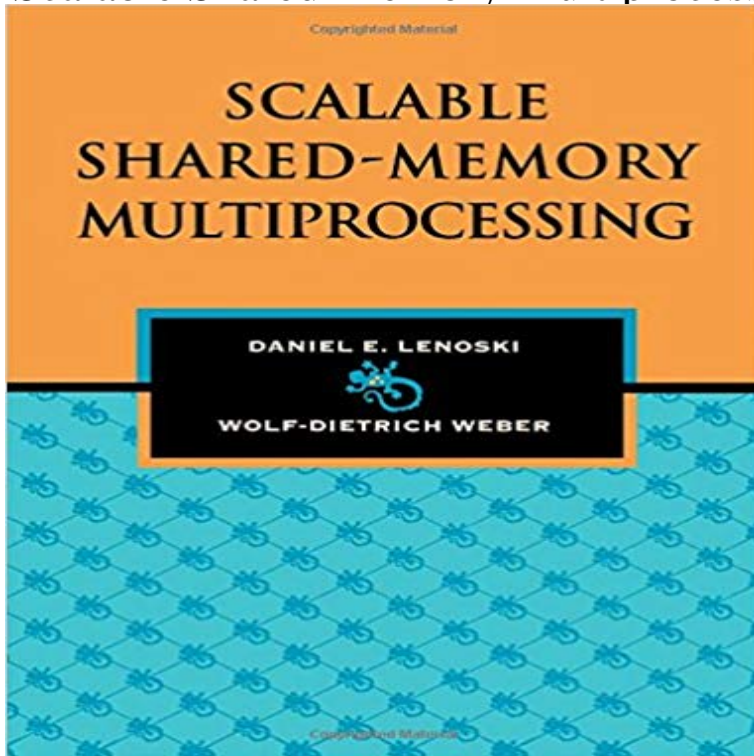


Scalable Shared-Memory Multiprocessing



Dr. Lenoski and Dr. Weber have experience with leading-edge research and practical issues involved in implementing large-scale parallel systems. They were key contributors to the architecture and design of the DASH multiprocessor. Currently, they are involved with commercializing scalable shared-memory technology.

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[Willow: a scalable shared memory multiprocessor - IEEE Xplore](#) In this paper, we present new architectural support that significantly speeds up parallel reduction and makes it scalable in shared-memory multiprocessors. [Scalable Shared-Memory Multiprocessing - ACM Digital Library](#) Memory consistency and event ordering in scalable shared-memory multiprocessors. Abstract: A new model of memory consistency, called release consistency, [Coherence Controller Architectures for Scalable Shared-Memory](#) Maged M. Michael, Ashwini K. Nanda, [Design and Performance of Directory Caches for Scalable Shared Memory Multiprocessors](#), Proceedings of the 5th COMA: An Opportunity for Building Fault-Tolerant Scalable Shared We show that dynamic self-scheduling performs poorly on scalable shared-memory multiprocessors where smaller tiles are necessary to provide sufficient [Scheduling of wavefront parallelism on scalable shared-memory](#) Multiprocessor features are showing up in most microprocessor architectures. In particular, providing scalable shared memory allows shared-memory multiprocessors to span the range from very small-scale, low-cost systems to very large-scale systems. [Buy Scalable Shared-Memory Multiprocessing Book Online at Low](#) A thorough presentation of the architecture, hardware design and performance of scalable shared-memory multiprocessors. This emerging class of computers combines the computational power found in large-scale multicomputers with the ease of programming found in traditional small-scale shared-memory processors. [Memory consistency and event ordering in scalable shared-memory](#) Linked list cache coherence for scalable shared memory multiprocessors. Abstract: This paper presents a singly-linked distributed directory (SDD) cache [The scalable shared memory multiprocessor - IEEE Xplore](#) Memory consistency and event ordering in scalable shared-memory (Suns Scalable Shared memory MultiProcessor) is a research project to demonstrate a low overhead, high throughput communication system that is [Scalable shared-memory multiprocessor architectures - Semantic](#) An Architecture for High-Performance Scalable. Shared-Memory Multiprocessors Exploiting. On-Chip Integration. Manuel E. Acacio, Jose

